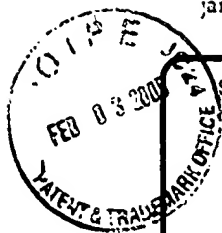


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Application / Conf. No.	09/724,972	/	7821
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Filing Date	November 28, 2000
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First Named Inventor	Stephen M. Trimberger
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Art Unit	2137
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Examiner Name	Minh Dieu T. Nguyen
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Attorney Docket Number	X-805-9 US
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U.S. PATENT DOCUMENTS

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FOREIGN PATENT DOCUMENTS

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**Examiner
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Date Considered

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				Filing Date	November 28, 2000
				First Named Inventor	Stephen M. Trimberger
				Art Unit	2137
				Examiner Name	Minh Dieu T. Nguyen
Sheet	2	of	2	Attorney Docket Number	X-805-9 US

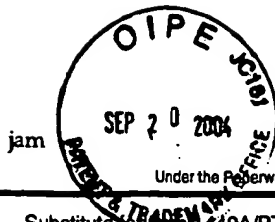
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mon		ARM; "Versatile FPGA and PLD configuration images", update 13 Sept 04, pg 1 of 1, available at http://www.arm.com/support/downloads/info/4804.html	
↓		XILINX, INC., News Release, September 20, 1999, pp 1-2, available from Xilinx, Inc., 2100 Logic Drive, San Jose, CA 95124 or; http://investor.xilinx.com/phoenix.zhtml?c=75919&p=irol-newsArticle&t=Regular&id=5214...	

Examiner Signature	<i>Nguyen Minh Dieu T.</i>	Date Considered	5/2/05
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Application / Conf. No.	09/724,972 / 7821
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Art Unit	2137
Examiner Name	Minh Dieu T. Nguyen
Attorney Docket Number	X-805-9 US

U.S. PATENT DOCUMENTS

Examiner Initials *	Cite No. *	Document Number	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		Number - Kind Code (if known)			
YMN		US- 5,838,901	08-05-96	Curd et al.	RECEIVED SEP 23 2004 Technology Center 2100
		US- 6,323,679	11-27-01	Robertson et al.	
		US- 2001/0032318	10-18-01	Yip et al.	
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		US- 6,212,639 B1	04-03-01	Erickson et al.	
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		US- 5,341,034	08-23-94	Matthews	
		US- 5,121,359	06-09-92	Steele	
		US- 5,388,157	02-07-95	Austin	
		US- 6,560,709	05-06-03	Galovich	

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		Country Code * -Number * -Kind Code * (if known)				
YMN		WO 00/49717	08-24-00	Trimberger		

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